

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/005,895
Filing Date	November 2, 2001
First Named Inventor	Awsienko, Oleg
Group Art Unit	2617
Examiner Name	Mehra, Inder

Attorney Docket No: 884.660US1

Sheet 1 of 1

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/IM/		LAW, K. L., et al., "A large scalable ATM multicast switch", <u>IEEE Journal on Selected Areas in Communications</u> , 15(5), (1997),844-854	
/IM/		MARCUS, W. S., et al., "A CMOS Batcher and banyan chip set for B-ISDN", <u>1990 IEEE International Solid-State Circuits Conference, 1990. Digest of Technical Papers. 37th ISSCC., (1990),32-33, 258</u>	
/IM/		MARCUS, W. S., "A CMOS Batcher and Banyan chip set for B-ISDN packet switching", <u>IEEE Journal of Solid-State Circuits</u> , 25(6), (1990),1426-1432	

EXAMINER

/Inder Mehra/

DATE CONSIDERED

04/08/2007

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached